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KEY=1 - CALEB KEY

FUNCTIONAL VERIFICATION OF DYNAMICALLY RECONFIGURABLE FPGA-BASED SYSTEMS

Springer This book analyzes the challenges in verifying Dynamically Reconfigurable Systems (DRS) with respect to the user design and the physical implementation of such systems. The authors describe the use of a simulation-only layer to emulate the behavior of target FPGAs and accurately model the characteristic features of reconfiguration. Readers are enabled with this simulation-only layer to maintain verification productivity by abstracting away the physical details of the FPGA fabric. Two implementations of the simulation-only layer are included: Extended Re Channel is a System C library that can be used to check DRS designs at a high level; ReSim is a library to support RTL simulation of a DRS reconfiguring both its logic and state. Through a number of case studies, the authors demonstrate how their approach integrates seamlessly with existing, mainstream DRS design flows and with well-established verification methodologies such as top-down modeling and coverage-driven verification.

FPGA IMPLEMENTATIONS OF NEURAL NETWORKS

Springer Science & Business Media During the 1980s and early 1990s there was significant work in the design and implementation of hardware neurocomputers. Nevertheless, most of these efforts may be judged to have been unsuccessful: at no time have hardware neurocomputers been in wide use. This lack of success may be largely attributed to the fact that earlier work was almost entirely aimed at developing custom neurocomputers, based on ASIC technology, but for such niche - eas this technology was never sufficiently developed or competitive enough to justify large-scale adoption. On the other hand, gate-arrays of the period mentioned were never large enough nor fast enough for serious artificial-neural-network (ANN) applications. But technology has now improved: the capacity and performance of current FPGAs are such that they present a much more realistic alternative. Consequently neurocomputers based on FPGAs are now a much more practical proposition than they have been in the past. This book summarizes some work towards this goal and consists of 12 papers that were selected, after review, from a number of submissions. The book is nominally divided into three parts: Chapters 1 through 4 deal with foundational issues; Chapters 5 through 11 deal with a variety of implementations; and Chapter 12 looks at the lessons learned from a large-scale project and also reconsiders design issues in light of current and future technology.

USING FINE GRAIN APPROACHES FOR HIGHLY RELIABLE DESIGN OF FPGA-BASED SYSTEMS IN SPACE

KIT Scientific Publishing

EMBEDDED COMPUTER SYSTEMS: ARCHITECTURES, MODELING, AND SIMULATION

5TH INTERNATIONAL WORKSHOP, SAMOS 2005, SAMOS, GREECE, JULY 18-20, PROCEEDINGS

Springer Science & Business Media This book constitutes the refereed proceedings of the 5th International Workshop on Systems, Architectures, Modeling, and Simulation, SAMOS 2005, held in Samos, Greece in July 2005. The 49 revised full papers presented were thoroughly reviewed and selected from 114 submissions. The papers are organized in topical sections on reconfigurable system design and implementations, processor architectures, design and simulation, architectures and implementations, system level design, and modeling and simulation.

HARDWARE/SOFTWARE CO-DESIGN AND OPTIMIZATION FOR CYBERPHYSICAL INTEGRATION IN DIGITAL MICROFLUIDIC BIOCHIPS

Springer This book describes a comprehensive framework for hardware/software co-design, optimization, and use of robust, low-cost, and cyberphysical digital microfluidic systems. Readers with a background in electronic design automation will find this book to be a valuable reference for leveraging conventional VLSI CAD techniques for emerging technologies, e.g., biochips or bioMEMS. Readers from the circuit/system design community will benefit from methods presented to extend design and testing techniques from microelectronics to mixed-technology microsystems. For readers from the microfluidics domain, this book presents a new design and development strategy for cyberphysical microfluidics-based biochips suitable for large-scale bioassay applications. • Takes a transformative, “cyberphysical” approach towards achieving closed-loop and sensor feedback-driven biochip operation under program control; • Presents a “physically-aware” system reconfiguration technique that uses sensor data at intermediate checkpoints to dynamically reconfigure biochips; • Enables readers to simplify the structure of biochips, while facilitating the “general-purpose” use of digital microfluidic biochips for a wider range of applications.

LOW-POWER DESIGN OF NANOMETER FPGAS

ARCHITECTURE AND EDA

Morgan Kaufmann Low-Power Design of Nanometer FPGAs Architecture and EDA is an invaluable reference for researchers and practicing engineers concerned with power-efficient, FPGA design. State-of-the-art power reduction techniques for FPGAs will be described and compared. These techniques can be applied at the circuit, architecture, and electronic design automation levels to describe both the dynamic and leakage power sources and enable strategies for codesign. Low-power techniques presented at key FPGA design levels for circuits, architectures, and electronic design automation, form critical, "bridge" guidelines for codesign Comprehensive review of leakage-tolerant techniques empowers designers to minimize power dissipation Provides valuable tools for estimating power efficiency/savings of current, low-power FPGA design techniques

THE SIBYLLINE ORACLES (ANNOTATED EDITION)

Jazzybee Verlag This is the extended and annotated edition including * an extensive annotation of almost 10.000 words about the oracles in religion * an interactive table-of-contents * perfect formatting for electronic reading devices THE Sibyls occupy a conspicuous place in the traditions and history of ancient Greece and Rome. Their fame was spread abroad long before the beginning of the Christian era. Heraclitus of Ephesus, five centuries before Christ, compared himself to the Sibyl "who, speaking with inspired mouth, without a smile, without ornament, and without perfume, penetrates through centuries by the power of the gods." The ancient traditions vary in reporting the number and the names of these weird prophetesses, and much of what has been handed down to us is legendary. But whatever opinion one may hold respecting the various legends, there can be little doubt that a collection of Sibylline Oracles was at one time preserved at Rome. There are, moreover, various oracles, purporting to have been written by ancient Sibyls, found in the writings of Pausanias, Plutarch, Livy, and in other Greek and Latin authors. Whether any of these citations formed a portion of the Sibylline books once kept in Rome we cannot now determine; but the Roman capitol was destroyed by fire in the time of Sulla (B. C. 84), and again in the time of Vespasian (A. D. 69), and whatever books were at those dates kept therein doubtless perished in the flames. It is said by some of the ancients that a subsequent collection of oracles was made, but, if so, there is now no certainty that any fragments of them remain.

SOCIAL QUALITY - LOOKING FOR A GLOBAL SOCIAL POLICY

BoD - Books on Demand Menschenrechtsdiskussionen, globale (Sozial-) Politik und die Bemühungen der UN für Menschliche Entwicklung ("Human Development") und Menschliche Sicherheit ("Human Security") geraten in zwei grundsätzliche "Sackgassen": Zum einen wird ein bestimmtes Modell von existierenden "entwickelten Staaten", d.h. mit hoch entwickeltem Kapitalismus, als einziges Ideal vorgestellt, ohne dass andere fortschrittliche Modelle ernsthaft berücksichtigt werden. Eine mögliche denkbare Alternative ist höchstens die romantisierende Vorstellung einer "guten" Gesellschaft, welche sich in vielen Fällen auf eine Art naive "Güte" einer indigenen Lebensweise bezieht. Zum anderen wird - nicht fern der erwähnten Alternativen - auf abstrakte Konzepte von Gerechtigkeit und Freiheit etc. hingewiesen, auf moralische Aussagen, die jedoch zur Trennung von systematischem sozioökonomischem ebenso wie politisch-rechtlichem Denken führen. Der vorliegende Band versucht, aus sozial-philosophischer Perspektive die politische Ökonomie und das Sozialrecht zusammenzuführen, um ein Verständnis von allgemeiner Verantwortung für soziale Qualität weltweit zu entwickeln. Besondere Beiträge sind eine Einführung in den theoretischen Ansatz der sozialen Qualität, ein Exkurs über islamische Gesellschaften und ein Exkurs hinsichtlich asiatischer Sichtweisen. Das Buch "Social Quality - Looking for a Global Social Policy" mit dem Untertitel "Attempting an Approximation to the Analysis of Distances of Social Systems" ist der zweite Band der Serie "Studien

zu vergleichender Sozialpädagogik und internationaler Sozialarbeit und Sozialpolitik", welche eine Fortsetzung der fast gleichnamigen Serie ist, die von Franz Ham-burger beim Schäuble Verlag, Rheinfelden herausgegeben wurde. Die Serie thematisiert problemorientiert sozialpädagogischen Theorien und die Arbeit sozialer Strukturen im internatio-nalen Rahmen und Vergleich. Dabei wird die Serie als ein Forum für Diskussionen und Ver-öffentlichungen begriffen, die eine Lösung der Definitionsproblematik sozialer Professionen anstrebt, welche - so der methodische Ansatz - nur im kritischen Vergleich und im internati-onalen Austausch erfolgen kann.

STRENGTHENING FORENSIC SCIENCE IN THE UNITED STATES

A PATH FORWARD

National Academies Press Scores of talented and dedicated people serve the forensic science community, performing vitally important work. However, they are often constrained by lack of adequate resources, sound policies, and national support. It is clear that change and advancements, both systematic and scientific, are needed in a number of forensic science disciplines to ensure the reliability of work, establish enforceable standards, and promote best practices with consistent application. Strengthening Forensic Science in the United States: A Path Forward provides a detailed plan for addressing these needs and suggests the creation of a new government entity, the National Institute of Forensic Science, to establish and enforce standards within the forensic science community. The benefits of improving and regulating the forensic science disciplines are clear: assisting law enforcement officials, enhancing homeland security, and reducing the risk of wrongful conviction and exoneration. Strengthening Forensic Science in the United States gives a full account of what is needed to advance the forensic science disciplines, including upgrading of systems and organizational structures, better training, widespread adoption of uniform and enforceable best practices, and mandatory certification and accreditation programs. While this book provides an essential call-to-action for congress and policy makers, it also serves as a vital tool for law enforcement agencies, criminal prosecutors and attorneys, and forensic science educators.

PROCEEDINGS OF SIXTH INTERNATIONAL CONGRESS ON INFORMATION AND COMMUNICATION TECHNOLOGY

ICICT 2021, LONDON, VOLUME 4

Springer Nature

'ADVANCES IN MICROELECTRONICS: REVIEWS', VOL_1

Lulu.com The 1st volume of 'Advances in Microelectronics: Reviews' Book Series contains 19 chapters written by 72 authors from academia and industry from 16 countries. With unique combination of information in each volume, the 'Advances in Microelectronics: Reviews' Book Series will be of value for scientists and engineers in industry and at universities. In order to offer a fast and easy reading of the state of the art of each topic, every chapter in this book is independent and self-contained. All chapters have the same structure: first an introduction to specific topic under study; second particular field description including sensing applications. Each of chapter is ending by well selected list of references with books, journals, conference proceedings and web sites. This book ensures that readers will stay at the cutting edge of the field and get the right and effective start point and road map for the further researches and developments.

RECONFIGURABLE LOGIC

ARCHITECTURE, TOOLS, AND APPLICATIONS

CRC Press During the last three decades, reconfigurable logic has been growing steadily and can now be found in many different fields. Field programmable gate arrays (FPGAs) are one of the most famous architecture families of reconfigurable devices. FPGAs can be seen as arrays of logic units that can be reconfigured to realize any digital systems. Their high versatility has enabled designers to drastically reduce time to market, and made FPGAs suitable for prototyping or small production series in many branches of industrial products. In addition, and thanks to innovations at the architecture level, FPGAs are now conquering segments of mass markets such as mobile communications. Reconfigurable Logic: Architecture, Tools, and Applications offers a snapshot of the state of the art of reconfigurable logic systems. Covering a broad range of architectures, tools, and applications, this book: Explores classical FPGA architectures and their supporting tools Evaluates recent proposals related to FPGA architectures, including the use of network-on-chips (NoCs) Examines reconfigurable processors that merge concepts borrowed from the reconfigurable domain into processor design Exploits FPGAs for high-performance systems, efficient

error correction codes, and high-bandwidth network routers with built-in security Expounds on emerging technologies to enhance FPGA architectures, improve routing structures, and create non-volatile configuration flip-flops Reconfigurable Logic: Architecture, Tools, and Applications reviews current trends in reconfigurable platforms, providing valuable insight into the future potential of reconfigurable systems.

DESIGN TECHNOLOGY FOR HETEROGENEOUS EMBEDDED SYSTEMS

Springer Science & Business Media Design technology to address the new and vast problem of heterogeneous embedded systems design while remaining compatible with standard "More Moore" flows, i.e. capable of simultaneously handling both silicon complexity and system complexity, represents one of the most important challenges facing the semiconductor industry today and will be for several years to come. While the micro-electronics industry, over the years and with its spectacular and unique evolution, has built its own specific design methods to focus mainly on the management of complexity through the establishment of abstraction levels, the emergence of device heterogeneity requires new approaches enabling the satisfactory design of physically heterogeneous embedded systems for the widespread deployment of such systems. Heterogeneous Embedded Systems, compiled largely from a set of contributions from participants of past editions of the Winter School on Heterogeneous Embedded Systems Design Technology (FETCH), proposes a necessarily broad and holistic overview of design techniques used to tackle the various facets of heterogeneity in terms of technology and opportunities at the physical level, signal representations and different abstraction levels, architectures and components based on hardware and software, in all the main phases of design (modeling, validation with multiple models of computation, synthesis and optimization). It concentrates on the specific issues at the interfaces, and is divided into two main parts. The first part examines mainly theoretical issues and focuses on the modeling, validation and design techniques themselves. The second part illustrates the use of these methods in various design contexts at the forefront of new technology and architectural developments.

A ROUTE TO CHAOS USING FPGAS

VOLUME I: EXPERIMENTAL OBSERVATIONS

Springer The purpose of this introductory book is to couple the teaching of chaotic circuit and systems theory with the use of field programmable gate arrays (FPGAs). As such, it differs from other texts on chaos: first, it puts emphasis on combining theoretical methods, simulation tools and physical realization to help the reader gain an intuitive understanding of the properties of chaotic systems. Second, the "medium" used for physical realization is the FPGA. These devices are massively parallel architectures that can be configured to realize a variety of logic functions. Hence, FPGAs can be configured to emulate systems of differential equations. Nevertheless maximizing the capabilities of an FPGA requires the user to understand the underlying hardware and also FPGA design software. This is achieved by the third distinctive feature of this book: a lab component in each chapter. Here, readers are asked to experiment with computer simulations and FPGA designs, to further their understanding of concepts covered in the book. This text is intended for graduate students in science and engineering interested in exploring implementation of nonlinear dynamical (chaotic) systems on FPGAs.

GUIDE FOR THE CARE AND USE OF LABORATORY ANIMALS

EIGHTH EDITION

National Academies Press A respected resource for decades, the Guide for the Care and Use of Laboratory Animals has been updated by a committee of experts, taking into consideration input from the scientific and laboratory animal communities and the public at large. The Guide incorporates new scientific information on common laboratory animals, including aquatic species, and includes extensive references. It is organized around major components of animal use: Key concepts of animal care and use. The Guide sets the framework for the humane care and use of laboratory animals. Animal care and use program. The Guide discusses the concept of a broad Program of Animal Care and Use, including roles and responsibilities of the Institutional Official, Attending Veterinarian and the Institutional Animal Care and Use Committee. Animal environment, husbandry, and management. A chapter on this topic is now divided into sections on terrestrial and aquatic animals and provides recommendations for housing and environment, husbandry, behavioral and population management, and more. Veterinary care. The Guide discusses veterinary care and the responsibilities of the Attending Veterinarian. It includes recommendations on animal procurement and transportation, preventive medicine (including animal biosecurity), and clinical care and management. The Guide addresses distress and pain recognition and relief, and issues surrounding euthanasia. Physical plant. The Guide identifies design issues, providing construction guidelines for functional areas; considerations such as drainage, vibration and noise control, and environmental monitoring; and specialized facilities for animal housing and research needs. The Guide for the Care

incorporated in engineering designs. A number of practical applications are discussed from areas such as data processing and vector-based computations (e.g. Hamming weight counters/comparators). The second part of the book covers the more theoretical aspects of finite state machine synthesis with the main objective of reducing basic FPGA resources, minimizing delays and achieving greater optimization of circuits and systems.

CAPITAL

Penguin UK The "forgotten" second volume of Capital, Marx's world-shaking analysis of economics, politics, and history, contains the vital discussion of commodity, the cornerstone to Marx's theories.

PROCEEDINGS

INTERNATIONAL SYMPOSIUM ON BIOMEDICAL ENGINEERING AND MEDICAL PHYSICS, 10-12 OCTOBER, 2012, RIGA, LATVIA

Springer Science & Business Media This volume presents the proceedings of the International Symposium on Biomedical Engineering and Medical Physics and is dedicated to the 150 anniversary of the Riga Technical University, Latvia. The content includes various hot topics in biomedical engineering and medical physics.

VOLTAGE REGULATORS FOR NEXT GENERATION MICROPROCESSORS

Springer Science & Business Media This book deals with energy delivery challenges of the power processing unit of modern computer microprocessors. It describes in detail the consequences of current trends in miniaturization and clock frequency increase, upon the power delivery unit, referred to as voltage regulator. This is an invaluable reference for anybody needing to understand the key performance limitations and opportunities for improvement, from both a circuit and systems perspective, of state-of-the-art power solutions for next generation CPUs.

RECONFIGURABLE COMPUTING: ARCHITECTURES AND APPLICATIONS

SECOND INTERNATIONAL WORKSHOP, ARC 2006, DELFT, THE NETHERLANDS, MARCH 1-3, 2006 REVISED SELECTED PAPERS

Springer Science & Business Media This book constitutes the thoroughly refereed post-proceedings of the Second International Workshop on Reconfigurable Computing, ARC 2006, held in Delft, The Netherlands, in March 2006. The 22 revised full papers and 35 revised short papers presented were thoroughly reviewed and selected from 95 submissions. The papers are organized in topical sections on applications, power, image processing, organization and architecture, networks and communication, security, and tools.

DIGITAL SIGNAL PROCESSING WITH FIELD PROGRAMMABLE GATE ARRAYS

Springer Science & Business Media Starts with an overview of today's FPGA technology, devices, and tools for designing state-of-the-art DSP systems. A case study in the first chapter is the basis for more than 30 design examples throughout. The following chapters deal with computer arithmetic concepts, theory and the implementation of FIR and IIR filters, multirate digital signal processing systems, DFT and FFT algorithms, and advanced algorithms with high future potential. Each chapter contains exercises. The VERILOG source code and a glossary are given in the appendices, while the accompanying CD-ROM contains the examples in VHDL and Verilog code as well as the newest Altera "Baseline" software. This edition has a new chapter on adaptive filters, new sections on division and floating point arithmetics, an up-date to the current Altera software, and some new exercises.

FIELD-PROGRAMMABLE LOGIC AND APPLICATIONS: RECONFIGURABLE COMPUTING IS GOING MAINSTREAM

RECONFIGURABLE COMPUTING IS GOING MAINSTREAM

Springer This book constitutes the refereed proceedings of the 12th International Conference on Field-Programmable Logic and Applications, FPL 2002, held in Montpellier, France, in September 2002. The 104 revised regular papers and 27 poster papers presented together with three invited contributions were carefully reviewed and selected from 214

submissions. The papers are organized in topical sections on rapid prototyping, FPGA synthesis, custom computing engines, DSP applications, reconfigurable fabrics, dynamic reconfiguration, routing and placement, power estimation, synthesis issues, communication applications, new technologies, reconfigurable architectures, multimedia applications, FPGA-based arithmetic, reconfigurable processors, testing and fault-tolerance, crypto applications, multitasking, compilation techniques, etc.

THE GOSPEL OF PSEUDO-MATTHEW AND THE NATIVITY OF MARY

Wipf and Stock Publishers The Gospel of Pseudo-Matthew is one of the most important witnesses in Western Europe to apocryphal stories about the lives of Mary, Joseph, Jesus, and Mary's parents, Anna and Joachim. As a bestseller of mainstream medieval Christianity, this Latin apocryphon is a keystone in the explosion of apocryphal literature in the Middle Ages. Despite its apocryphal status, the Gospel of Pseudo-Matthew remained both popular and influential throughout the Middle Ages and into the early modern period, as its popularity and influences may be traced in Christian literature, visual arts, liturgy, and theological perspectives still revered by Roman Catholic theologians. The gospel is also a significant work for considering the history of monasticism and the cult of the Virgin Mary. This book presents the first English translation of the Gospel of Pseudo-Matthew with a full introduction and commentary, as well as translations of related works with accompanying commentaries.

THE RIGHTS OF WAR AND PEACE

INCLUDING THE LAW OF NATURE AND OF NATIONS

REVIEW OF MARITIME TRANSPORT 2020

This series contains the decisions of the Court in both the English and French texts.

CRYPTOGRAPHIC HARDWARE AND EMBEDDED SYSTEMS - CHES 2004

6TH INTERNATIONAL WORKSHOP CAMBRIDGE, MA, USA, AUGUST 11-13, 2004, PROCEEDINGS

Springer Science & Business Media This book constitutes the refereed proceedings of the 6th International workshop on Cryptographic Hardware and Embedded Systems, CHES 2004, held in Cambridge, MA, USA in August 2004. The 32 revised full papers presented were carefully reviewed and selected from 125 submissions. The papers are organized in topical sections on side channels, modular multiplication, low resources, implementation aspects, collision attacks, fault attacks, hardware implementation, and authentication and signatures.

TRANSACTIONS ON INTELLIGENT WELDING MANUFACTURING

VOLUME I NO. 2 2017

Springer The primary aim of this volume is to provide researchers and engineers from both academia and industry with up-to-date coverage of recent advances in the fields of robotic welding, intelligent systems and automation. It gathers selected papers from the 2017 International Workshop on Intelligentized Welding Manufacturing (IWIWM'2017), held June 23-26, 2017 in Shanghai, China. The contributions reveal how intelligentized welding manufacturing (IWM) is becoming an inescapable trend, just as intelligentized robotic welding is becoming a key technology. The volume is divided into four main parts: Intelligent Techniques for Robotic Welding, Sensing in Arc Welding Processing, Modeling and Intelligent Control of Welding Processing, and Intelligent Control and its Applications in Engineering.

HIGH-PERFORMANCE COMPUTING USING FPGAS

Springer Science & Business Media High-Performance Computing using FPGA covers the area of high performance reconfigurable computing (HPRC). This book provides an overview of architectures, tools and applications for High-Performance Reconfigurable Computing (HPRC). FPGAs offer very high I/O bandwidth and fine-grained, custom and flexible parallelism and with the ever-increasing computational needs coupled with the frequency/power wall, the increasing maturity and capabilities of FPGAs, and the advent of multicore processors which has caused the acceptance of parallel computational models. The Part on architectures will introduce different FPGA-based HPC platforms: attached co-processor

HPRC architectures such as the CHREC's Novo-G and EPCC's Maxwell systems; tightly coupled HPRC architectures, e.g. the Convey hybrid-core computer; reconfigurably networked HPRC architectures, e.g. the QPACE system, and standalone HPRC architectures such as EPFL's CONFETTI system. The Part on Tools will focus on high-level programming approaches for HPRC, with chapters on C-to-Gate tools (such as Impulse-C, AutoESL, Handel-C, MORA-C++); Graphical tools (MATLAB-Simulink, NI LabVIEW); Domain-specific languages, languages for heterogeneous computing (for example OpenCL, Microsoft's Kiwi and Alchemy projects). The part on Applications will present case from several application domains where HPRC has been used successfully, such as Bioinformatics and Computational Biology; Financial Computing; Stencil computations; Information retrieval; Lattice QCD; Astrophysics simulations; Weather and climate modeling.

ATOMIC HABITS

AN EASY & PROVEN WAY TO BUILD GOOD HABITS & BREAK BAD ONES

Penguin The #1 New York Times bestseller. Over 4 million copies sold! *Tiny Changes, Remarkable Results* No matter your goals, Atomic Habits offers a proven framework for improving--every day. James Clear, one of the world's leading experts on habit formation, reveals practical strategies that will teach you exactly how to form good habits, break bad ones, and master the tiny behaviors that lead to remarkable results. If you're having trouble changing your habits, the problem isn't you. The problem is your system. Bad habits repeat themselves again and again not because you don't want to change, but because you have the wrong system for change. You do not rise to the level of your goals. You fall to the level of your systems. Here, you'll get a proven system that can take you to new heights. Clear is known for his ability to distill complex topics into simple behaviors that can be easily applied to daily life and work. Here, he draws on the most proven ideas from biology, psychology, and neuroscience to create an easy-to-understand guide for making good habits inevitable and bad habits impossible. Along the way, readers will be inspired and entertained with true stories from Olympic gold medalists, award-winning artists, business leaders, life-saving physicians, and star comedians who have used the science of small habits to master their craft and vault to the top of their field. Learn how to: • make time for new habits (even when life gets crazy); • overcome a lack of motivation and willpower; • design your environment to make success easier; • get back on track when you fall off course; ...and much more. Atomic Habits will reshape the way you think about progress and success, and give you the tools and strategies you need to transform your habits--whether you are a team looking to win a championship, an organization hoping to redefine an industry, or simply an individual who wishes to quit smoking, lose weight, reduce stress, or achieve any other goal.

EMBEDDED SOFTWARE DESIGN AND PROGRAMMING OF MULTIPROCESSOR SYSTEM-ON-CHIP

SIMULINK AND SYSTEM C CASE STUDIES

Springer Science & Business Media Current multimedia and telecom applications require complex, heterogeneous multiprocessor system on chip (MPSoC) architectures with specific communication infrastructure in order to achieve the required performance. Heterogeneous MPSoC includes different types of processing units (DSP, microcontroller, ASIP) and different communication schemes (fast links, non standard memory organization and access). Programming an MPSoC requires the generation of efficient software running on MPSoC from a high level environment, by using the characteristics of the architecture. This task is known to be tedious and error prone, because it requires a combination of high level programming environments with low level software design. This book gives an overview of concepts related to embedded software design for MPSoC. It details a full software design approach, allowing systematic, high-level mapping of software applications on heterogeneous MPSoC. This approach is based on gradual refinement of hardware/software interfaces and simulation models allowing to validate the software at different abstraction levels. This book combines Simulink for high level programming and SystemC for the low level software development. This approach is illustrated with multiple examples of application software and MPSoC architectures that can be used for deep understanding of software design for MPSoC.

THE BOOK OF THE SECRETS OF ENOCH

Health Research Books 1896 Translated from the Slavonic by W.R. Morfill. Reader in Russian and the other Slavonic languages. Edited, with introductory notes & indices by R. H. Charles, M.A., Trinity college, Dublin & Exeter college, Oxford. (1896). the Secrets of Enoch is.

EMBEDDED SYSTEM DESIGN: TOPICS, TECHNIQUES AND TRENDS

IFIP TC10 WORKING CONFERENCE: INTERNATIONAL EMBEDDED SYSTEMS SYMPOSIUM (IESS), MAY 30 - JUNE 1, 2007, IRVINE (CA), USA

Springer This volume presents the technical program of the 2007 International Embedded Systems Symposium held in Irvine, California. It covers timely topics, techniques and trends in embedded system design, including design methodology, networks-on-chip, distributed and networked systems, and system verification. It places emphasis on automotive and medical applications and includes case studies and special aspects in embedded system design.

EMBEDDED COMPUTER SYSTEMS: ARCHITECTURES, MODELING, AND SIMULATION

9TH INTERNATIONAL WORKSHOP, SAMOS 2009, SAMOS, GREECE, JULY 20-23, 2009, PROCEEDINGS

Springer Science & Business Media This book constitutes the refereed proceedings of the 9th International Workshop on Architectures, Modeling, and Simulation, SAMOS 2009, held on Samos, Greece, on July 20-23, 2009. The 18 regular papers presented were carefully reviewed and selected from 52 submissions. The papers are organized in topical sections on architectures for multimedia, multi/many cores architectures, VLSI architectures design, architecture modeling and exploration tools. In addition there are 14 papers from three special sessions which were organized on topics of current interest: instruction-set customization, reconfigurable computing and processor architectures, and mastering cell BE and GPU execution platforms.

RECONFIGURABLE COMPUTING: ARCHITECTURES, TOOLS AND APPLICATIONS

THIRD INTERNATIONAL WORKSHOP, ARC 2007, MANGARATIBA, BRAZIL, MARCH 27-29, 2007, PROCEEDINGS

Springer This book constitutes the refereed proceedings of the Third International Workshop on Applied Reconfigurable Computing, ARC 2007, held in Mangaratiba, Brazil, in March 2007. The 27 full papers and 10 short papers presented together with a late-comer contribution from ARC 2006 are organized in topical sections on architectures, mapping techniques and tools, arithmetic, and applications.

ATTENTION AND EFFORT

Prentice Hall